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616 Chapter 8 Feedback i i v o + - M 1 5 k Ω 10 k Ω 2 3 Figure 848 An ac schematic of a shunt-shunt feedback amplifier 87 The ac schematic of a
shunt-shunt feed- back amplifier is shown in Fig 848 All transistors have $I_D = 1\text{mA}$, $W/L = 100$, $k = 60\text{ A/V}^2$, and $\lambda = 1/(50\text{ V})$ (a) Calculate the
overall gain v_o/i_i , the loop transmission, the input impedance, and the output

ANALYSIS AND DESIGN OF ANALOG INTEGRATED CIRCUITS

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Analog Integrated Circuit Design

EE 215A HO#1 Fall 2014 Analog Integrated Circuit Design Instructor: Behzad Razavi 56-147D, Eng IV

LECTURE 01 - INTRODUCTION TO CMOS ANALOG CIRCUIT DESIGN

analog circuit design are used for simplifying the analysis or design The goal of an The goal of an assumption is to separate the essential information
from the nonessential

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