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Fundamentals of Digital Logic with Verilog Design

classical manual digital design and (2) illustrate clearly the way in which digital circuits are designed today, using CAD tools Even though modern designers no longer use manual techniques, except in rare circumstances, our motivation for teaching such techniques is to give students an intuitive feeling for how digital circuits operate Also, the manual techniques provide an illustration of

Advanced Digital Design with the Verilog HDL, Second Edition

Advanced Digital Design with the Verilog HDL, Second Edition Michael D Ciletti Prentice Hall, Pearson Education, 2011 Problem 2-1 Recall that a minterm is a cube in which every variable appears A Boolean expression in SOP form is canonical if every cube in the expression has a unique representation in which all of the literals are in complemented or uncomplemented form $F(a, b, c$

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