

# Soc H 265 Hevc Video Audio Encoder Ip Core Datasheet

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### Soc H 265 Hevc Video

#### **SOC H.265/HEVC Video/Audio Encoder IP Core Datasheet**

The SOC H265 HEVC video encoder IP core is a single chip solution, which is designed to support single or multi-stream H265 video encoding for all industrial standard resolutions including QVGA, SD, HD up to 1080p@120, and 4K@60 (8K@60 will be supported) It currently supports up to 1080@60, and 4K@60 Higher resolutions will be released in the near future The SOC H265 video encoder IP core

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Soc H 265 Hevc Video SOC H265/HEVC Video/Audio Encoder IP Core Datasheet The SOC H265 HEVC video encoder IP core is a single chip solution, which is designed to support single or multi-stream H265 video encoding for all industrial standard resolutions including QVGA, SD, HD up to 1080p@120, and 4K@60 (8K@60 will be supported) It currently supports up Specification Sheet - SOC ...

#### **Specification Sheet - SOC Technologies inc.**

H265 Video/Audio Encoder Chipset SOC provides High-performance H265/HEVC encoder IP cores for both Xilinx and Intel (Altera) FPGAs SOC also configures the cores according to user specifications, including I/O formats SOC also supplies all-in-one hardware modules and ASIC Chipsets, based on the IP cores The SOC codec modules are System -on

#### **MSC316Q High-Integrated H.265/HEVC IP Camera SoC Processor**

Video Encoding Performance Supports 1920x1080p30 + VGAp30 + QCIFp30 H265/HEVC or H264 encoding Supports MJPEG 2M (1920x1080) 15fps encoding Audio Processor One stereo ADC for microphone inputs One stereo DAC for lineouts Supports 8K/16K/32KHz sampling rate audio recording

Digital and analog gain adjustment

### **Multicore DSPs for High-Performance Video Coding**

new High Efficiency Video Coding (HEVC or H265) standard The standard is still currently under definition but is expected to be ratified very soon HEVC improves data compression by up to 50% over the current state-of-the-art and with the increasing amount of high-definition (HD) video traffic, this is a huge benefit In addition to providing flexibility in the network, there

### **ENCODING LIVE AND VOD FOR HEVC/HLS**

Video Streams • H265 • H264 streams (For backward compatibility some video content SHOULD be encoded with H264) Trick Play Streams • H264 • H265 (not specified, but Apple has for both) • Dedicated encodes are preferred, but can use existing file Configuration (HEVC) • Main 10, Level 5, High • Level 5 peaks at 30 fps (says same as

### **Xilinx Advanced Multimedia Solutions with Video Codec ...**

High Efficiency Video Coding (HEVC), or H265 compression, is becoming a key enabler of UHD; it promises to provide up to a 50% increase in bandwidth and storage savings compared to the currently deployed H264 compression standard The bandwidth efficiency associated with the adoption of HEVC promises to greatly expand delivery of high-quality, high-resolution video over bandwidth

### **H2 Video SoC for Consumer Applications**

The Ambarella H2 Video SoC for consumer applications integrates an advanced image sensor pipeline (ISP), H265 10-bit (HEVC-10) and H264 (AVC) encoders, and a powerful Quad core ARM® Cortex™-A53 CPU for advanced analytics, flight control, WiFi streaming, and other user applications Targeting the next generation of consumer applications such as connected drones, sports cameras, and 360°

### **Ambarella S3 IP Camera SoC Delivers Outstanding 4K Video ...**

HD video processing semiconductors, today introduced S3, a security IP camera System-On-Chip (SoC) family with support for the new H265 or HEVC (High Efficiency Video Coding) video standard S3's full implementation of H265 delivers up to twice the compression efficiency of Ambarella's current H264 IP camera SoCs, providing outstanding video quality at very low bitrates to optimize network

### **M550S Camera SoC - VATICS**

H265/H264 Encoder SoC with Hardware Fisheye Dewarping Confidential (ISP) ® a s advanced imaging DR, DWDR, 3D and RTOS-based easy product such as sensor and lens applications Image Sensor Image Sensor Image Sensor ARM® Cortex A5 (720MHz 32KB/32KB) /F OTG I/F /1000 /F IrDA I/F UART I/F x5 GPIO PWM x12 Video Input I/F (1) Bayer (2) MIPI Rx (3) SubLVDS / HiSpi (4) BT1120 ...

### **Socionext Starts Sampling of 4K/p60 HEVC Video Encoder Chip;**

world's first HEVC/H265 - compatible encoder IC, the MB86M31, along with an evaluation board, the MB86M31-EVB, and the software development kit for drivers and applications The new device is capable of real-time encoding of 4K/p60 video with the latest video compression technology HEVC achieves the transmission of realistic 4K video at half the cost of the preceding H264 technology

### **System-On-Chip (SOC) Technologies Inc.**

• H265 Main 12 Profile 4:2:2/4:2:0 HD (1080P@120) 4k (3840x2160)@30/60 • H264 High Profile 4:2:2/4:2:0 HD (1080P@30/60/120) 4k (3840x2160)@30/60 • MPEG-2 High Profile 4:2:2/4:2:0 HD (1080P@30/60/120) Features: • Low latency -025ms • Low power -500mw • Small footprint -60kAMLS • High video quality soc@soctechnologies.com System-On-Chip Technologies -The power of system

### **H22 Video SoC for Consumer Applications**

system-on-chip that integrates an advanced image sensor pipeline (ISP), H265 (HEVC) and H264 (AVC) encoders , and a powerful Quad core ARM® Cortex™-A53 CPU for advanced analytics, computer vision, flight control, WiFi streaming, and other user applications Targeting the next generation of connected drones, sports, and 360° (VR) cameras, the H22 delivers up to 4K-video recording at 60fps

### **Emerging Video Technologies H.265, SVC and WebRTC**

H265 • H265 is a video compression standard -HEVC (High Efficiency Video Coding) -MPEG-H Part 2 • H264's successor • Under joint development by Joint Collaborative Team on Video Coding (JCT-VC) - ISO/IEC Moving Picture Experts Group (MPEG) - ITU-T Video Coding Experts Group (VCEG)

### **VEGA-3310 4K HRVC & AVC Broadcast Video Encoding/ Decoding ...**

21/05/2018 · The technology underlying VEGA-3310 is the latest encoding/decoding SoC Each device supports HEVC, AVC, and MPEG2 real-time encoding, decoding, and transcoding at up to 4Kp60 with 10 bit colour depth and 4:2:2 chroma sampling HEVC compression is particularly relevant for 4K UltraHD transmission which requires a much higher stream capacity These bandwidth reduction ...

### **H22AQ Video SoC for Automotive**

Video SoC for Automotive Video Input HiSPi™ Video Output DDR Interface Audio Input (I2S / ADC) Audio Output (I2S) SD / SDIO NAND / NOR DMA UART I2C SPI USB 20 GPIO Host / Device Timers WDT Ethernet DDR3 / DDR3L SD Card / eMMC Sensor 1 Sensor 2 Analog Parallel MIPI CSI-2 Parallel HDMI 32-BIT LVDS SLVS Image Signal Processor (ISP) Video Codec AVC HEVC MJPEG 80211n ...

### **PR2016050 Socionext Introduces 8K HEVC Real-Time Encoder ...**

Yokohama, November 10, 2016 --- Socionext Inc, a leader in SoC-based solution for video and imaging systems, today introduced the world's first solution that enables the encoding of HEVC/H265 video at 8K/60p with a single board, by utilizing the company's latest codec ICs Socionext will start delivering the solution at the end of December The solution will be showcased at InterBEE

### **FPGA-Based Framework for Hardware Acceleration of the HEVC ...**

HEVC (High Efficiency Video Coding), the most recent standard for video compression, has led to an huge increase of the encoding efficiency with the respect to the previous standards but, on the other hand, several tasks have become highly time demanding and power consuming This thesis proposes a fast, dynamically reconfigurable and flexible hardware accelerator for the SATD (Sum of

### **Hi3798C V200 Brief Data Sheet**

Video Decoding (HiVXE 20 Processing Engine) z H265/HEVC Main/Main10 profile@level 51 high-tier, maximum 4K x 2K@60 fps and 1x1080p@30 fps simultaneous decoding z H264/AVC BP/MP/HP@level 51; H264/AVC MVC, maximum 4K x 2K@30 fps decoding z VP9, maximum 4K x 2K@60 fps decoding z VP6/8, maximum 1080p@60 fps decoding z MPEG1, maximum 1080p@60 fps decoding z MPEG2 ...